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such as arithmetic, classified in class 378, subclass 62.

*Group VI*: Claims 68-89 are drawn to two dimensional light array, classified in class 436, subclass 43.

*Group VII*: Claims 90-125 are drawn to method, classified in class 353, subclass 20, and class 345, subclass 467.

*Group VIII*: Claims 126-130 are drawn to digital backplane storing, classified in class 701, subclass 213-215.

*Group IX*: Claims 131-141 are drawn to array of circuits, classified in class 708, subclass 130.

*Group X*: Claims 144-157 are drawn to a spatial light modulator, classified in class 714, subclass 31,800.

*Group XI*: Claims 158-160 are drawn to mapping of first and second pixel values, classified in class 347, subclass 239, 241.

*Group XII*: Claims 161-168 are drawn to MRAM array storage bits, classified in class 711, subclass 163 and class 359, subclass 244.

As agreed during a telephone interview between Applicants' counsel and the Examiner on September 15, 2004, claim 15, which was not addressed in the Restriction Requirement, should be included in Group I. Applicants elect the claims of Group I, *i.e.*, claims 1 through 15, for prosecution in the instant application. Applicants reserve the right to file divisional applications for the subject matter covered by the remaining groups. The inventors for the invention of the elected claims is the same as the inventors of record in the application.

With respect to the objection to the Abstract set forth as part of the September 1, 2004, Office Action, this objection is addressed in the Amendment accompanying the present response.

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In view of the foregoing, it is respectfully submitted that this application is now in condition for allowance and favorable action is respectfully solicited.

Respectfully submitted,

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September 29, 2004